

RCE 1-2133

#12/5-10-04  
v Jones



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takaki YOSHIDA et al.

Group Art Unit: 2133

Serial No.: 09/697,305

Examiner: Joseph D. Torres

Filed: October 27, 2000

For: FAULT DETECTING METHOD AND LAYOUT METHOD  
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

PETITION FOR EXTENSION OF TIME AND  
TRANSMITTAL OF FEE UNDER 37 C.F.R. §1.17

RECEIVED

MAY 05 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2100

Sir:

Transmitted herewith is a response to the outstanding Official Action in the above-identified application. The shortened statutory period having expired April 2, 2004, it is hereby requested that the period for response be extended for one (1) month. There is also attached our Check No. 17604 in the amount of \$110.00 in payment for:

XX Extension fee for response within first month  
pursuant to §1.136(a) (\$110.00) [\$55.00].

Extension fee for response within second month  
pursuant to §1.136(a) (\$420.00) [\$210.00].

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Extension fee for response within third month  
pursuant to §1.136(a) (\$950.00) [\$475.00].

The Commissioner is hereby authorized to charge any additional fee (or credit any overpayment) associated with this communication to Deposit Account No. 16-0331. A duplicate copy of this letter is enclosed.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

April 30, 2004  
Date

  
Roger W. Parkhurst  
Registration No. 25,177  
Robert N. Wieland  
Registration No. 40,225

RWP:RWP/mhs

Attorney Docket No.: YMOR:186

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PLEASE ACCEPT THIS AS  
AUTHORIZATION TO DEBIT  
OR CREDIT FEES TO  
DEP. ACCT. 16-0331  
PARKHURST & WENDEL